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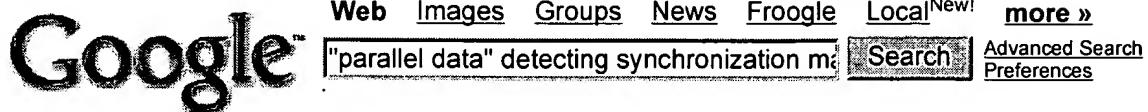
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Patent 5003559: Digital transmission system

... 14 for **detecting** a synchronizing signal contained in a first digital audio
 ... whereby **parallel data** PDATA having 16 bits per channel, as shown in FIG. ...
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... and/or real-time strip out for analysis and **mark** event reporting. ...
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Database Design Techniques

... cross-instance **synchronization**, the more efficient your application. ...
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... The byte **parallel data** presented by a conventional parallel output port is converted ... they don't require a second wire to carry **synchronization** data. ...
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... **Parallel data** are group of digital bits that are all available at the same ...
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kiuchi

... I have developed a new correlation-processing algorithm for **parallel data**.
 ... Figure 4: Data **synchronization** sequence in case of real-time VLBI. ...
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... mechanical, for dealing with **parallel data** transmission ... SYNC DETECT En.

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... Due to the multi-wire interconnection, **parallel data** transfer is ...

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... Unlike odd and even parity, fake parity is not capable of **detecting** an invalid

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... An efficient technique for migrating the **synchronization** operations is ...

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... are used to properly adjust the maximum rate of change of the **mark** bits, ...

The response frame consists of three bytes of **parallel data** with byte 0 and ...

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Digital multiplexer.

... The framing units (2) respond to a common block **synchronization** signal from a

... framing means (14, 15, 25, 26, 28) for framing the **parallel data** from ...

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... The device automatically switches to the external **synchronization** mode when

... The CE **mark** are shown with an incombustible sticker on the side of the ...

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EP1001635 Sony european software patent - Data recording apparatus ...

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... with existing sequential and **parallel data** management systems, ...

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Backward Error Recovery in Redundant Disk Arrays

... and hence **synchronization**, of shared resources in order to complete. ...

The array must detect the presence of an error during a disk access, ...

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... We give an axiomatization of the global **synchronization** statement, ...

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... convert **parallel data** streams into serial data streams and vice versa. ...

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Synchronization. Fan Jiang and Albert MK Cheng, ...

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... **synchronization**. This clock is a symmetrical square wave at the sampling frequency ... make it possible to either detect errors or correct errors. ...

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... Enhancing Data Migration Performance via **Parallel Data** Compression. ...

Programs on NOWs with **Synchronization** Requirements at the Application Level. ...

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... A Context Switch Reduction Technique for Real-time Task **Synchronization**. ...

Fast Algorithm for **Detecting** Distributed Deadlocks in the OR Request Model. ...

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... **re-synchronization** of the data transfer to an external clock system. RDCLK.

parallel data is registered at the rising edge of RDCLK. RDCLK is derived ...

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... A key issue in designing an event builder is the **synchronization** between the levels ... An SCI ring with multiple producers and **parallel data** transfers ...

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... the time and talent to invest in features like **parallel data** management tools,

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... Because ISIS' detection technique focusses on **detecting** symptoms of a ...

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... by multiplexing on-chip **parallel data** into a single serial bitstream. ...

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5

... Serial data transfer is much slower than the **parallel data** transfer offered

... is once more in its **mark** state and is ready to send the next character. ...

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... **Parallel data** bit. RQ11,NRQ11 is the most significant bit. ... **synchronization**.

Normally tied to RSTN. When not used ... A1 Ball ID **Mark** ...

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... deserialize the incoming bitstream, detect the 10-bit **synchronization**.

character in the bitstream, align the **parallel data** on word boundaries, ...

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... It converts the 8 bit **parallel data** from the microprocessor into signals that are sent ... Additional **synchronization** between the software and hardware. ...

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... Process of taking a **parallel data** format n-bits wide and transmitting at one

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... can detect the collision)? This is a problem in process **synchronization**. ...

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... sary to help developers detect this kind of object. Optimizelt has been chosen in the ... machines without **synchronization** and molecule ex-. change. ...

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... case a behavioral description is composed of **parallel data**. ow operations.

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... are set to TXSEL2 = 1 and TXSEL1 = TXLEL0, the modem enters the **parallel data** mode. ... the analog chip should provide **synchronization** means between the ...

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... erasure flag is set for each symbol to **mark** that all. symbols from C1 are unreliable at that ... **Synchronization** will be an essential aspect of inter- ...

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... The programmer must manage communication, **synchronization**, ... may be computed by a parallel subgraph obtained by **detecting** inter-object parallelism. ...

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... but we are in need of 2D **parallel data** to use the relations derived above.

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budget for clock domain transfer and multi-link de-skew/**synchronization** ...
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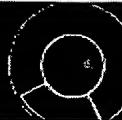
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WHITEHILL, Eric, A. / DEMPSEY, Tim / ITT MANUFACTURING ENTERPRISES, INC., PATENT COOPERATION TREATY APPLICATION, May 2001

...COORDINATING CHANNEL ACCESS TO SHARED **PARALLEL DATA CHANNELS** CROSS REFERENCE TO RELATED...coordinate channel access to plural **parallel data** channels thereby permitting efficient...channel access to multiple shared **parallel data** channels to permit timely and...

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...mark. On detecting the first synchronization mark, the disk...transferring data after it...only one synchronization mark detected...transferring data at the later...fails and no data is transferred...the invalid synchronization mark. -_, - 15...
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BEHRENS, Richard, T. / ANDERSON, Kent, D. / ARMSTRONG, Alan / DUDLEY, Trent / FOLAND, Bill / GLOVER, Neal / KING, Larry / CIRRUS LOGIC, INC., PATENT COOPERATION TREATY APPLICATION, Aug 1994
...error-tolerant synchronization and channel quality...provide decoded data using sequence...error-tolerant sync mark detection and...ability to recover data when the sync mark is obliterated...provided in an N-bit parallel form as digitized read data DRDO and DRD1...
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...processed. Sector mark sub-field 103...indicate that the data region associated...sub-field, a sector mark sub-field, a second...prerecorded servo data. This servo field...automatic gain control data. Further, the...field. The sector mark field of this...sub-field used for synchronization is designed so...
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...emission sensing system, having use in **detecting** and/or measuring one or more components...emissions measurement system collects **data** by a means other than a direct connection...about an axis 19. Also preferably, a **synchronization** device 58 is provided to track the position...

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